

Refine Search

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Search: L23 Refine Search

Recall Text Clear Interrupt

Search History

DATE: Friday, March 04, 2005 [Printable Copy](#) [Create Case](#)

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<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
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<u>L22</u> L20 and l3	0	<u>L22</u>
<u>L21</u> L20 and l11	0	<u>L21</u>
<u>L20</u> l15 and l10	62	<u>L20</u>
<u>L19</u> l10 and L18	16	<u>L19</u>
<u>L18</u> L16 and l5	56	<u>L18</u>
<u>L17</u> L16 and l2	2	<u>L17</u>
<u>L16</u> 710/.ccls.	21840	<u>L16</u>
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<u>L14</u> L10 same (bus master\$1)	9	<u>L14</u>
<u>L13</u> L11 same switch\$3	9	<u>L13</u>
<u>L12</u> L11 and l10	2	<u>L12</u>
<u>L11</u> (address\$3 near3 command\$1) near4 lock\$5	133	<u>L11</u>
<u>L10</u> pci same debug\$5	357	<u>L10</u>

<u>L9</u>	(device select\$5 signal\$1) and l2	5	<u>L9</u>
<u>L8</u>	debug\$5 and L7	21	<u>L8</u>
<u>L7</u>	pci same L5	22	<u>L7</u>
<u>L6</u>	display\$5 same L5	6	<u>L6</u>
<u>L5</u>	lock\$5 same (address\$2 near3 data near3 command\$1)	167	<u>L5</u>
<u>L4</u>	(lock\$5 near3 (signal state\$1)) same debug\$5	1	<u>L4</u>
<u>L3</u>	(send\$5 near3 (request signal\$1)) same bus cycle\$1	13	<u>L3</u>
<u>L2</u>	(peripheral component interconnect\$1) same debug\$5	30	<u>L2</u>
<u>L1</u>	(single-step interruption\$1) same debug\$5	3	<u>L1</u>

END OF SEARCH HISTORY